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DOCKET NO.: 2789.1001-001

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

William J. Dally, Ramin Farjad-Rad, Teva J. Stone, Xiaoying Yu and John W. Poulton

Application No.:

09/849,920

Group Art Unit:

2816

Filed:

May 4, 2001

Examiner:

Cassandra F. Cox

Confirmation No.:

1489

Title:

COMBINED PHASE COMPARATOR AND CHARGE PUMP CIRCUIT

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

(Filed after payment of issue fee)

Sir:			
This II	information Disclosure Statement is submitted: under 37 CFR 1.129(a), or (First/Second submission after Final Rejection)		
[]	under 37 CFR 1.97(b), or (Within any one of the following time periods: three months of filing stage in an international application; or before the mailing date of a factor CPA, or a Request for Continued Examination).	irst office action on the merits	
[X]	under 37 CFR 1.97(c) together with either: [] a Statement under 37 CFR 1.97(e), as	01 FC:1806 checked below, or	180.00 GP
	[X] a \$180.00 fee under 37 CFR 1.17(p), of (After the 3.7 CFR 1.97(b) time period, but before final action or not		curs first)
[]	under 37 CFR 1.97(d) together with:		
-	[] a Statement under 37 CFR 1.97(e), as	checked below, and	
-	[] a \$180.00 fee under 37 CFR 1.17(p), c (Filed after final action or notice of allowance, whichever occurs first		the issue fee)
[]	under 37 CFR 1.97(i): Applicant requests that the IDS and cited refe	erence(s) be placed in	the application filewrapper.

Statem	ent Unc	<u>der 37 C</u>	FR 1.97(e)
[]	any co	mmunic	nformation contained in this Information Disclosure Statement was first cited in ation from a foreign patent office in a counterpart foreign application not more this prior to the filing of this Information Disclosure Statement; or
[]	No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.		
Statem	ent Uno	<u>der 37 C</u>	(Patent Term Adjustment) Applies to original applications (other than design) filed on or after May 29, 2000
[]	commitwas no	unication ot receive	information contained in the Information Disclosure Statement was cited in a in from a foreign patent office in a counterpart application and this communication ed by any individual designated in § 1.56(c) more than thirty days prior to the formation Disclosure Statement.
[X]	Enclos	sed herev	with is form PTO-1449:
	[X]	Copies	of the cited references are enclosed.
		[]	Since this application was filed after June 30, 2003, copies of issued U.S. patents and published U.S. applications are not required and are not being provided.
	[]	Applica	of the cited references are enclosed except those entered in prior application, U.S. ation No. [], to which priority under 35 U.S.C. 120 is claimed. [The earlier tion contains copies of the cited references.]
	[]	The list	ted references were cited in the enclosed International Search Report in a rpart foreign application.
	[]	The "co	oncise explanation" requirement (non-English references) for reference(s) [37 CFR 1.98(a)(3) is satisfied by:
		[]	the explanation provided on the attached sheet.
		[]	the explanation provided in the Specification.
		[]	submission of the enclosed International Search Report.
		[]	submission of the enclosed English-language version of a foreign Search Report and/or foreign Office Action.
		[]	the enclosed English language abstract.

[]	Α	applic	cant requests that the following r	non-published pending applic	cations be considered:
Examiner'	s				
	-		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
	-		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
	-		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
			Examiner	Date	_
	[]	A copy of each above-cited app	olication, including the curre	ent claims, is enclosed.
	[]	A copy of each above-cited app those entered in prior application 35 U.S.C. 120 is claimed.		ent claims, is enclosed, except], to which priority under
The E	xaı nce	miner es we	r is requested to return a copy of re considered with the next office	the above list of pending ap	plications indicating which
It is re	equ	ested	that the information disclosed h	nerein be made of record in t	his application.
Metho	od (of pay	yment:		
[X]			ck for the fee noted above is enc panying Reply. A copy of this S		cluded in the check with the
[]		lease	charge Deposit Account 08-038 ed.	30 in the amount of \$[]. A copy of this Statement is
[X]	P	lease	charge any deficiency in fees ar	nd credit any overpayment to	Deposit Account 08-0380.
			I	Respectfully submitted,	
			I	HAMILTON, BROOK, SMI	TH & REYNOLDS, P.C.
			I	James M. Smith Registration No.: 28,043 Telephone: (978) 341-0036 Facsimile: (978) 341-0136	

Concord, MA 01742-9133 Dated: 5/28/4

PTO-1449 REPRODUCED	ATTORNEY DOCKET NO. 2789.1001-001	APPLICATION NO. 09/849,920		
May 28, 2004 JUN 0 1 2004 May 28, 2004 JUN 0 1 2004 May 28 if necessary)	FIRST NAMED INVENTOR William J. Dally FILING DATE May 4, 2001		1	
	EXAMINER Cassandra F. Cox	CONF 1489	IRMATION NO.	GROUP 2816

	U.S. PATENT DOCUMENTS					
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER Number-Kind Code (if known)	ISSUE DATE / PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT		
	AF	6,114,915	09/05/2000	Huang et al.		
	AG	5,361,254	11/01/1994	Storck et al.		
	АН	6,208,183 B1	03/27/2001	Li et al.		
	AI	6,037,812	03/14/2000	Gaudet		
	AJ	5,646,564	07/08/1997	Erickson et al.		
	AK	6,043,717	03/28/2000	Kurd		
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EXAMINER	DATE CONSIDERED

PTO-1449 REPRODUCED	ATTORNEY DOCKET NO. 2789.1001-001	APPLICATION NO. 09/849,920		
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION IN AN APPLICATION	FIRST NAMED INVENTOR William J. Dally FILING DATE May 4, 200		1	
May 28, 2004 Se several sheets if necessary)	EXAMINER Cassandra F. Cox	CONF1	IRMATION NO.	GROUP 2816

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
	AS	Dally, William J. and Poulton, John W., <i>Digital Systems Engineering</i> , Cambridge University Press, 1998, pp. 537-540 and 547-548.			
	ΑТ	Kim, Weigant and Gray, "PLL/DLL System Noise Analysis for Low Jitter Clock Synthesizer Design," ISCAS, 1994, pp. 31-34.			
	AU	Waizman, A., "A Delay Line Loop for Frequency Synthesis of De-Skewed Clock," IEEE International Solid-State Circuits Conference, 1994, pp. 298-299.			
	AV	Dally, William J. and Poulton, John W., "Transmitter Equalization for 4Gb/s Signaling," IEEE Micro, Jan-Feb 1997, pp. 48-56.			
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EXAMINER	DATE CONSIDERED